

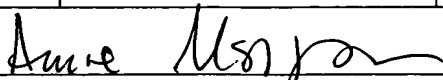
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|------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-----------------------------------|--------------------------------------------------------------------|
| Form PTO-1449 (REV. 8-83) US Dept. of Commerce PATENT & TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary) | ATTY DOCKET NO. 038404.02 | NEW RULE 1.53(b) DIVISIONAL OF APPLICATION NO. 09/218,499 |
| | APPLICANT(S) Seiichiro HIGASHI | |
| | FILING DATE December 27, 2001 | GROUP |

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 12/27/01

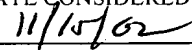
U.S. PATENT DOCUMENTS

| EXAMINER INITIAL | | DOCUMENT NUMBER | DATE | NAME | CLASS | SUB CLASS |
|---------------------|--|-----------------|---------|------------------|-------|--------------|
| A-M | | 5,623,279 | 4/1997 | ITAKURA et al. | | |
| | | 5,250,931 | 10/1993 | MISAWA et al. | | |
| | | 5,192,945 | 3/1993 | KUSADA | 345 | 100 |
| | | 5,432,529 | 7/1995 | AZUHATA | | |
| | | 5,532,712 | 7/1996 | TSUDA et al. | | |
| | | 5,162,786 | 11/1992 | FUKUDA | 345 | 100 |
| | | 5,166,671 | 11/1992 | MAEKAWA | 345 | 100 |
| | | 5,461,424 | 10/1995 | SHIMIZU | | |
| | | 5,335,023 | 8/1994 | EDWARDS | | |
| | | 5,583,535 | 12/1996 | TAKARADA et al. | | |
| | | 5,051,739 | 9/1991 | HAYASHIDA et al. | 345 | 98 |
| | | 5,682,175 | 12/94 | KITAMURA | 345 | 100 |
| | | 5,883,609 | 10/95 | ASADA et al. | 345 | 100 |
| | | 5,850,204 | 02/90 | MAEKAWA | 345 | 100 |
| | | 5,818,413 | 02/96 | HAYASHI et al. | 345 | 100 |
| | | 5,523,772 | 05/94 | LEE | 345 | 98 |
| | | 5,534,885 | 12/93 | SAITOH | 345 | 98 |
| | | 5,251,051 | 10/93 | FUJIYOSHI et al. | 345 | 98 |
| | | 5,754,152 | 11/95 | NAKAMURA et al. | 345 | 98 |
| | | 5,038,139 | 08/91 | FUJISAWA et al. | 345 | 98 |
| | | 5,248,963 | 09/93 | YASUI et al. | 345 | 98 |
| | | 5,448,259 | 09/95 | HIDAKA | 345 | 98 |
| | | 5,528,267 | 12/89 | ISE | 345 | 174 |
| | | 5,418,551 | 05/94 | ISE | 345 | 174 |
| A-M | | 5,283,556 | 12/89 | ISE | 345 | 174 |

EXAMINER



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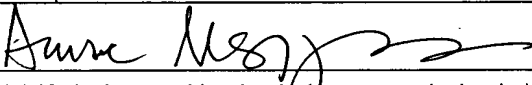
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| FOREIGN PATENT DOCUMENTS | | | | | | |
|--------------------------|-----|----------------------|-----------------|---------|---------------------|-----------|
| | | DOCUMENT NUMBER | DATE | COUNTRY | CLASS | SUB CLASS |
| A-M | | JP-A-2-74990 | 3/1990 | Japan | [Handwritten marks] | |
| | | JP-A-5-35221 | 2/1993 | Japan | | |
| | | JP-A-63-161495 | 7/1988 | Japan | | |
| | | JP-A-5-265411 | 10/1993 | Japan | | |
| | | JP-A-61-32093 | 2/1986 | Japan | | |
| | | JP-A-64-18193 | 1/1989 | Japan | | |
| | | JP-A-63-52121 | 3/1988 | Japan | | |
| | | JP-A-6-43490 | 2/1994 | Japan | | |
| | | JP-A-58-11995 | 1/1983 | Japan | | |
| | | JP-5-40319 | 6/1993 | Japan | | |
| | | JP-8-27463 | 3/1996 | Japan | | |
| | | 60-52892 (Laid-Open) | 3/1985 | Japan | | |
| | A-M | | EP 0 525 980 A2 | 2/1993 | | Europe |
| | | EP 0 525 980 A3 | 2/1993 | Europe | | |

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|--------------------------------|-----------------------------------|
| EXAMINER <i>Anne McGinn</i> | DATE CONSIDERED <i>4/15/02</i> |
|--------------------------------|-----------------------------------|

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| | | | | FILING DATE December 27, 2001 | | GROUP | |
| OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.) | | | | | | | |
| A · M | | S. INOUE et al., "S16-2 425°C Poly-Si TFT Technology and Its Applications to Large Size LCDs and Integrated Digital Date Drivers", <i>ASIA Display</i> , 1995, pp. 339-342. | | | | | |
| | | Y. MATSUEDA et al., "30.1: A 6-bit-Color VGA Low-Temperature Poly-Si TFT-LCD with Integrated Digital Data Drivers", <i>SID 98 Digest</i> , 1998, pp. 879-882. | | | | | |
| | | Y. MATSUEDA et al., "4.2: Low-Temperature Poly-Si TFT-LCD with Integrated 6-bit Digital Data Drivers", <i>SID 96 Digest</i> , 1996, pp. 21-24. | | | | | |
| | | Y. NISHIHARA et al., "32.5 Fully Integrated Poly Si TFT CMOS Drivers for Self-Scanned Light Valve", <i>SID DIGEST</i> (1992), pp. 609-612. | | | | | |
| A · M | | DA COSTA et al., "Amorphous Silicon Shift Register For Addressing Output Drivers", <i>IEEE Journal of Solid-State Circuits</i> , 1994; pp. 596-599. | | | | | |
| | | | | | | | |
| EXAMINER | | | |  | | DATE CONSIDERED 11/5/02 | |
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